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USPTO (571) 273-2885
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FROM: Pat Thomas for James Clingan (512) 996-6839
 (SENDER) (EXTENSION)

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Docket No.: SC11499TS

Applicant: Alex S. Koh et al.

Serial No.: 09/847,487

Art Unit: 2123

Filed: May 2, 2001

Class: 703-014000

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MAR 16 2006

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Pat Thomas

(Depositor's name)

3-16-05

(Signature)

(Date)

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 09/847,487 | 05/02/2001 | Alex S.Y. Koh | SC11499TS | 6254 |

TITLE OF INVENTION: METHOD FOR PRODUCING TEST PATTERNS FOR TESTING AN INTEGRATED CIRCUIT

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1400 | \$300 | \$1700 | 03/22/2006 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|-----------------------|----------|----------------|
| THANGAVELU, KANDASAMY | 2123 | 703-01400D |

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- James L. Clingan, Jr.
 Joanna G. Chiu
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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FREESCALE SEMICONDUCTOR, INC. AUSTIN, TEXAS

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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- a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.
 b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

James L. Clingan, Jr.

Typed or printed name

James L. Clingan, Jr.

Date *March 16, 2006*

Registration No. 30,163

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